

ABSTRACT OF THE DISCLOSURE

A bit line is connected to a data circuit for use in a program/read time. The data circuit includes first, second, and third data storage units. The first
5 data storage unit is connected to the bit line. A first data transfer circuit is connected between the first and third data storage units. A second data transfer circuit is connected between the second and third data storage units. The second data storage unit
10 has a function of forcibly changing a value of data of the first data storage unit based on the data stored in the second data storage unit.